

**FIG.1**

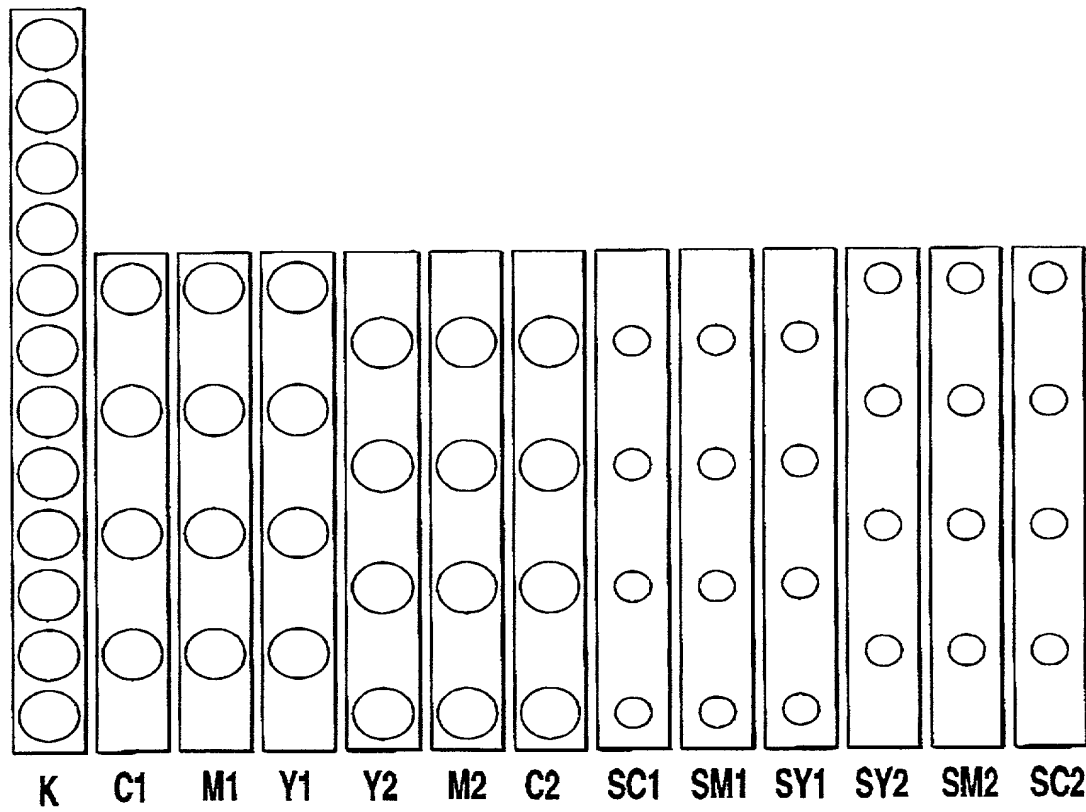


FIG.2

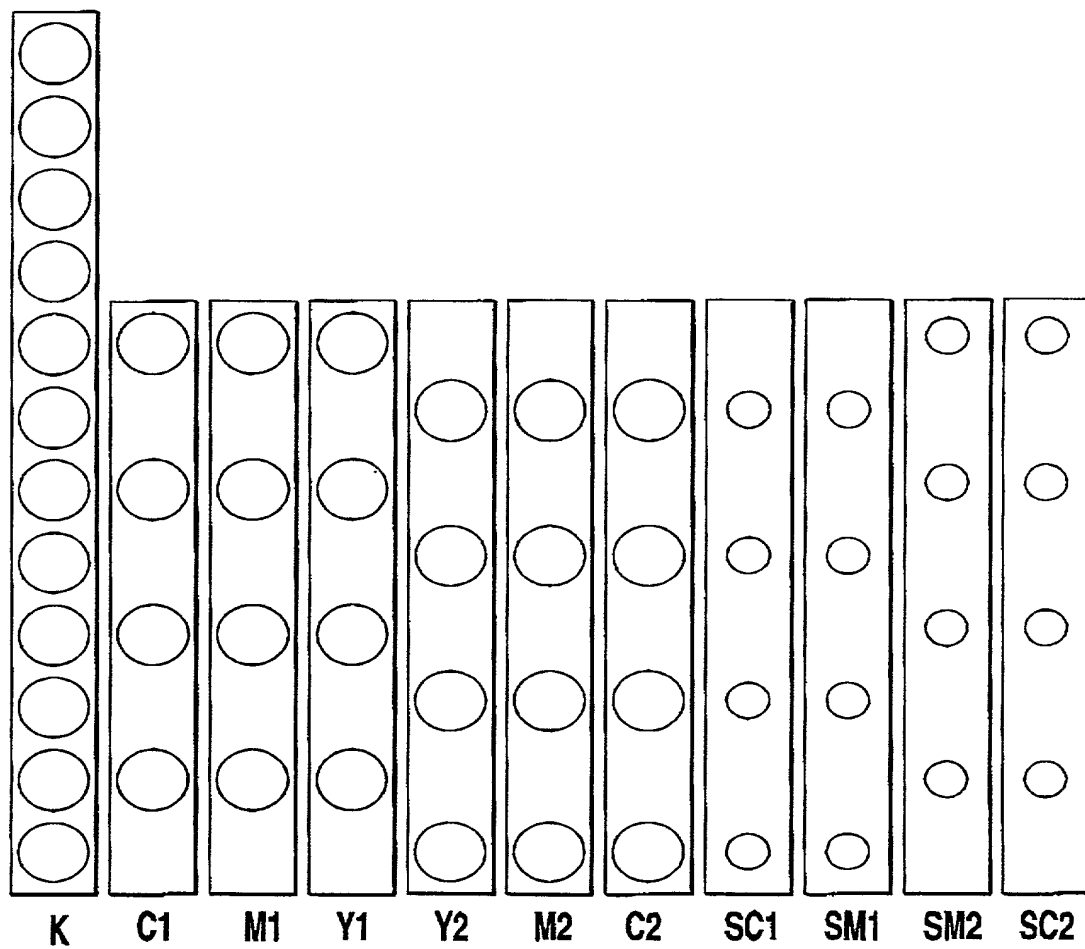
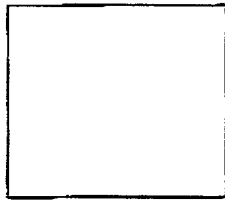
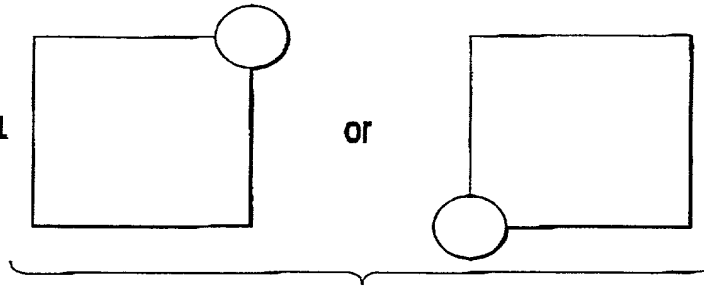


FIG.3

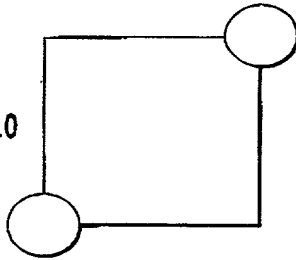
LEVEL 0  
BIT SIGNAL: 0000

**FIG.4A**

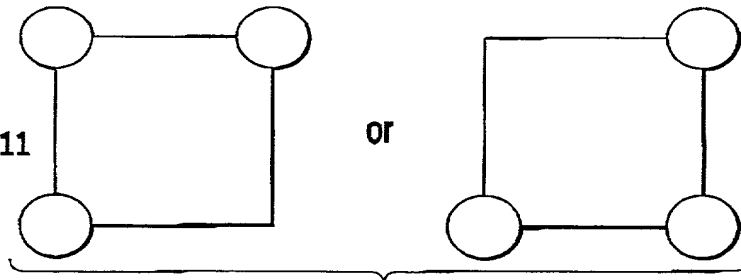
LEVEL 1  
BIT SIGNAL: 0001

**FIG.4B**

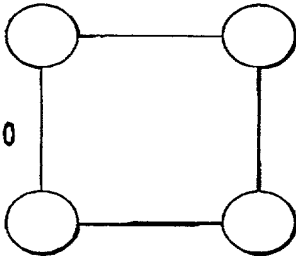
LEVEL 2  
BIT SIGNAL: 0010

**FIG.4C**

LEVEL 3  
BIT SIGNAL: 0011

**FIG.4D**

LEVEL 4  
BIT SIGNAL: 0100

**FIG.4E**

LEVEL 5  
BIT SIGNAL: 0101

or

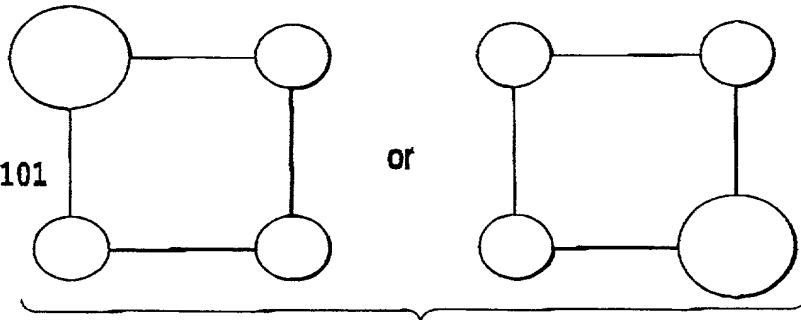


FIG.5A

LEVEL 6  
BIT SIGNAL: 0110

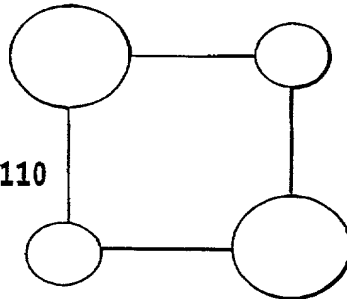


FIG.5B

LEVEL 7  
BIT SIGNAL: 0111

or

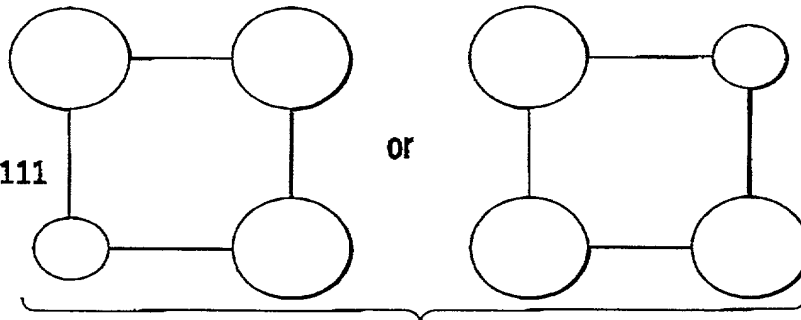


FIG.5C

LEVEL 8  
BIT SIGNAL: 1000

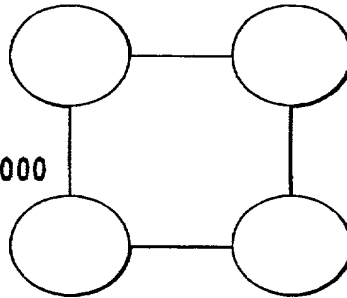


FIG.5D

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RELATIONSHIP BETWEEN INPUT LEVEL AND LANDED RATE  
OF INK IN THE CASE WHERE SAME PLANE DATA FOR  
RESPECTIVE LARGE AND SMALL DROPLETS  
(SAME INDEX PATTERNS FOR RESPECTIVE LARGE  
AND SMALL DROPLETS) ARE USED

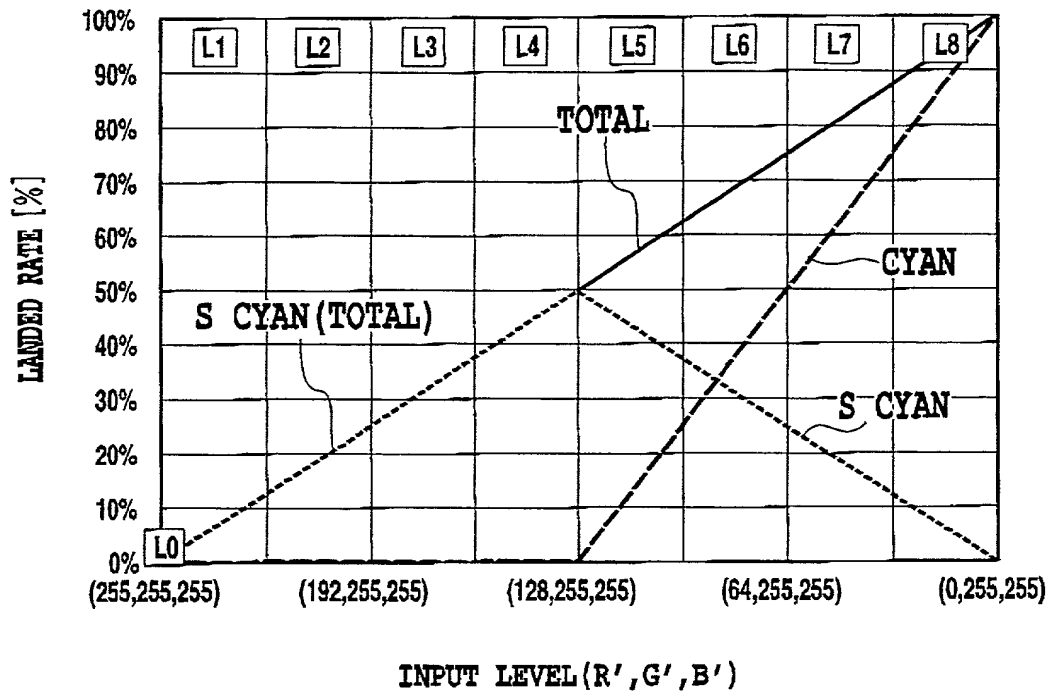


FIG.6

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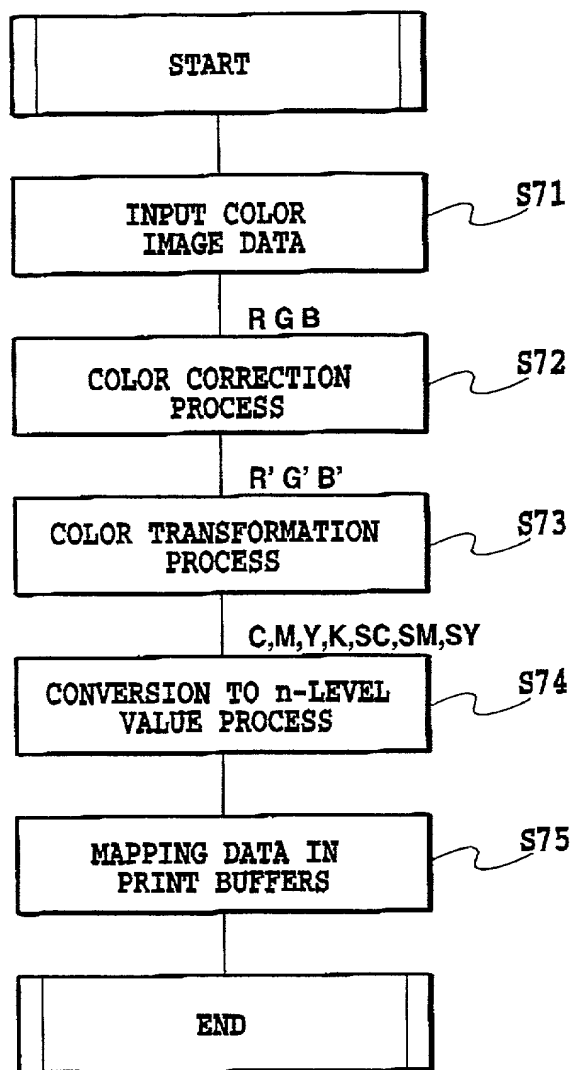


FIG.7

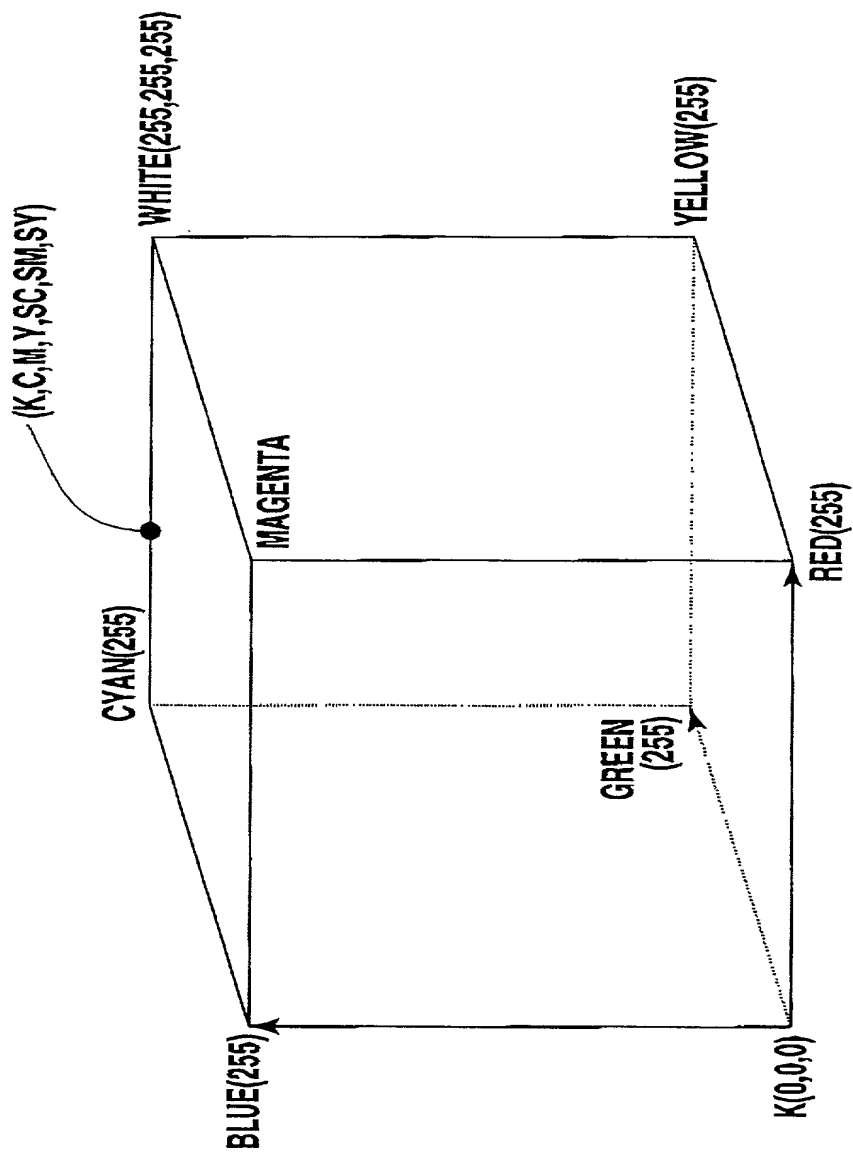


FIG.8



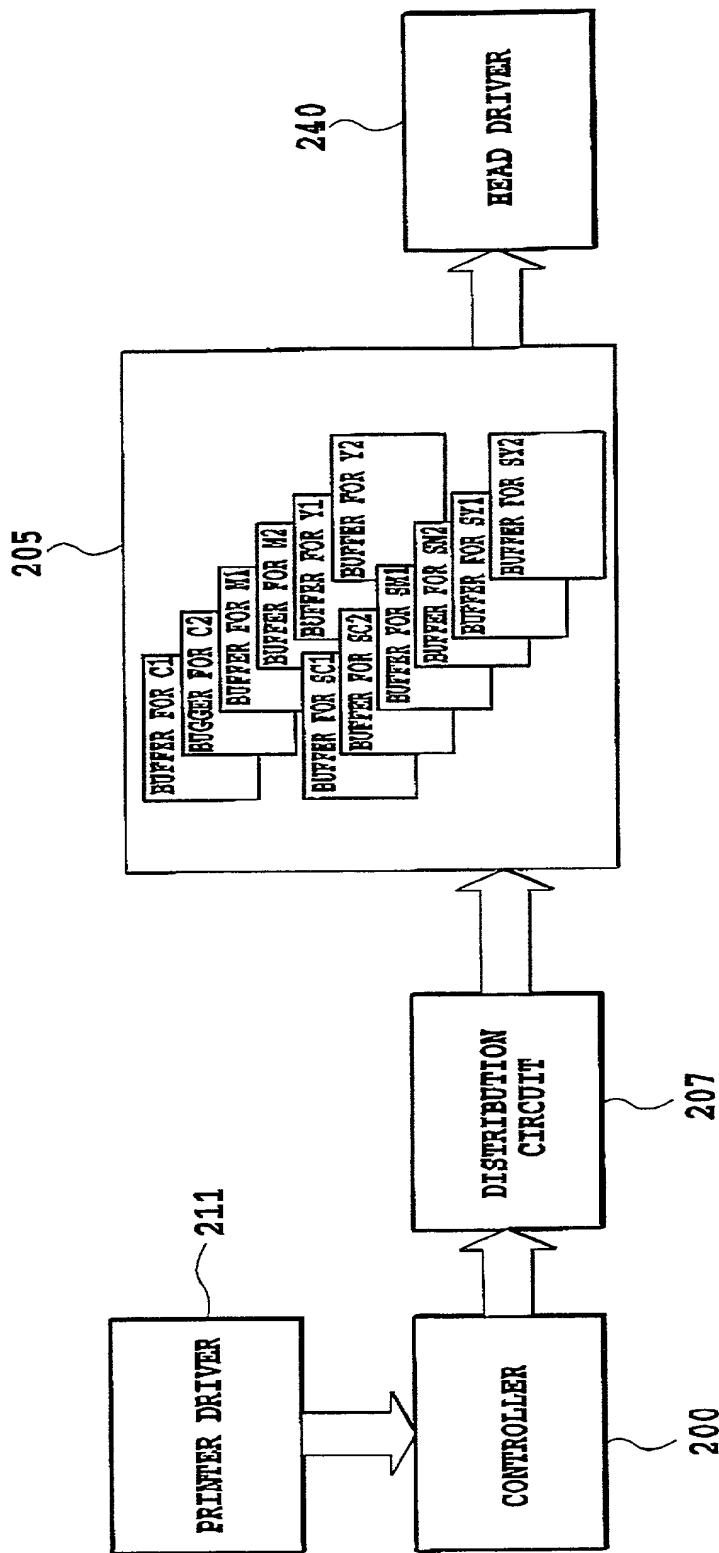


FIG.9

INPUT AND OUTPUT SIGNALS TO AND FROM COLOR  
TRANSFORMATION PROCESS SECTION  
(IN THE CASE OF USING INDEPENDENT DATA  
FOR RESPECTIVE LARGE AND SMALL DROPLETS)

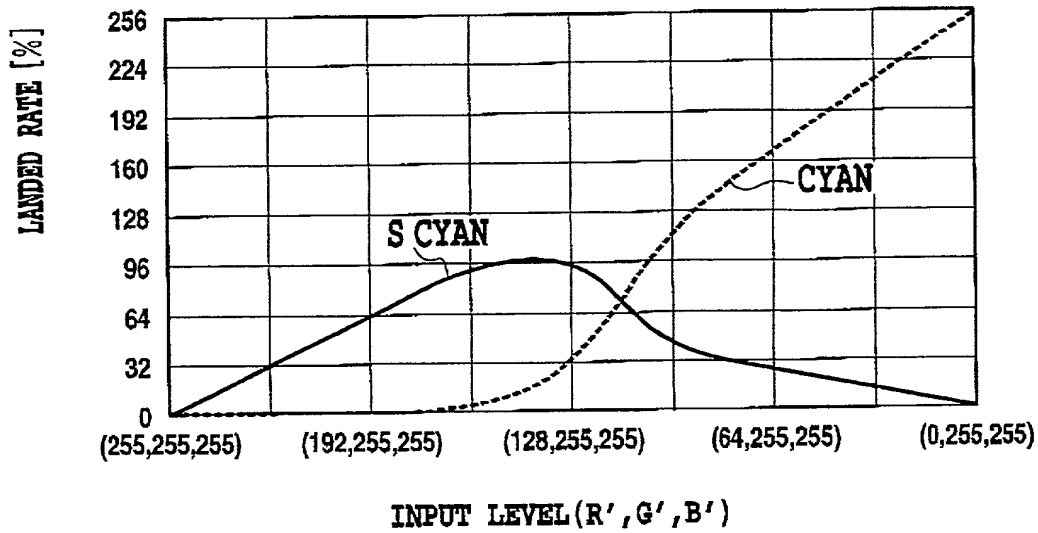


FIG.10A

RELATIONSHIP BETWEEN INPUT LEVEL AND LANDED  
RATE IN CASE OF USING INDEPENDENT DATA FOR  
RESPECTIVE LARGE AND SMALL DROPLETS  
(INDEPENDENT INDEX PATTERNS FOR RESPECTIVE  
LARGE AND SMALL DROPLETS)

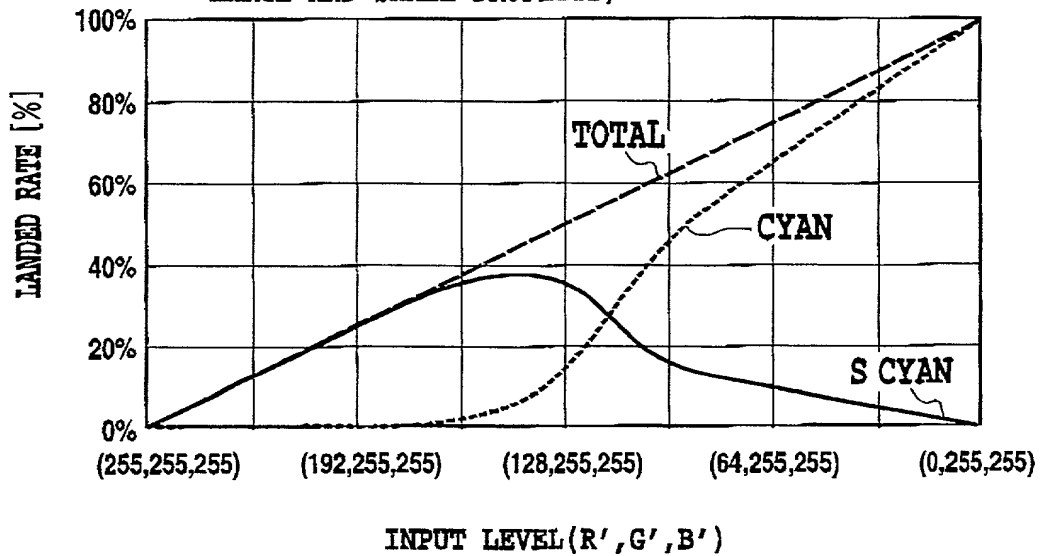


FIG.10B

